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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO. CONFIRMATION NO.	
10/522,502	11/11/2005	Werner Ertle	I431.124.101/	1444
25281 DICKE, BILLIO	7590 10/08/201 G & CZAJA	EXAMINER		
FIFTH STREE	ΓTOWERS	HUBER, ROBERT T		
100 SOUTH FL MINNEAPOLI	FTH STREET, SUITE S, MN 55402	ART UNIT	PAPER NUMBER	
			2892	
			MAIL DATE	DELIVERY MODE
			10/08/2010	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/522,502	ERTLE ET AL.	
Examiner	Art Unit	
	Artonit	

	ROBERT HUBER	2892	
The MAILING DATE of this communication appe	ars on the cover sheet with the c	correspondence add	ress
THE REPLY FILED <u>24 September 2010</u> FAILS TO PLACE THIS	S APPLICATION IN CONDITION F	OR ALLOWANCE.	
1.  The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following rapplication in condition for allowance; (2) a Notice of Apple for Continued Examination (RCE) in compliance with 37 C periods:	eplies: (1) an amendment, affidavit al (with appeal fee) in compliance	t, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
a) The period for reply expiresmonths from the mailing	date of the final rejection.		
b) The period for reply expires on: (1) the mailing date of this Adno event, however, will the statutory period for reply expire la Examiner Note: If box 1 is checked, check either box (a) or (I	ter than SIX MONTHS from the mailing	g date of the final rejection	n.
MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f	).		
Extensions of time may be obtained under 37 CFR 1.136(a). The date of have been filed is the date for purposes of determining the period of extra under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the size forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	ension and the corresponding amount on the corresponding amount of the corresponding a	of the fee. The appropria nally set in the final Offic	ate extension fee e action; or (2) as
2. ☐ The Notice of Appeal was filed on A brief in compl	iance with 37 CFR 41 37 must be t	filed within two months	s of the date of
filing the Notice of Appeal (37 CFR 41.37(a)), or any exten Notice of Appeal has been filed, any reply must be filed with AMENDMENTS	sion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	
	ut prior to the data of filing a brief	will not be entered be	001100
<ol> <li>The proposed amendment(s) filed after a final rejection, be (a) They raise new issues that would require further con (b) They raise the issue of new matter (see NOTE below</li> </ol>	sideration and/or search (see NOT		cause
(c) They are not deemed to place the application in bett appeal; and/or	•	lucing or simplifying t	ne issues for
(d) $\square$ They present additional claims without canceling a c	orresponding number of finally reje	ected claims.	
NOTE: (See 37 CFR 1.116 and 41.33(a)).			
4. The amendments are not in compliance with 37 CFR 1.12		mpliant Amendment (l	PTOL-324).
<ul><li>5. Applicant's reply has overcome the following rejection(s):</li><li>6. Newly proposed or amended claim(s) would be allowed.</li></ul>	<del></del>	imaly filad amandmar	ot concoling the
non-allowable claim(s).	owabie ii subifiitted iii a separate, t	illiely filed afficildifier	it canceling the
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is prove The status of the claim(s) is (or will be) as follows:  Claim(s) allowed:  Claim(s) objected to:  Claim(s) rejected: 18,19,22-33,38,39 and 41-44.  Claim(s) withdrawn from consideration: 34-37.		l be entered and an e:	xplanation of
AFFIDAVIT OR OTHER EVIDENCE			
<ol> <li>The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e).</li> </ol>			
<ol> <li>The affidavit or other evidence filed after the date of filing a entered because the affidavit or other evidence failed to over showing a good and sufficient reasons why it is necessary</li> </ol>	/ercome <u>all</u> rejections under appea and was not earlier presented. Se	ıl and/or appellant fail: ee 37 CFR 41.33(d)(1	s to provide a ).
10. The affidavit or other evidence is entered. An explanation	of the status of the claims after er	ntry is below or attach	ed.
<ul> <li>REQUEST FOR RECONSIDERATION/OTHER</li> <li>11. The request for reconsideration has been considered but See Continuation Sheet.</li> </ul>	does NOT place the application in	condition for allowan	ce because:
12. Note the attached Information <i>Disclosure Statement</i> (s). (13. Other:	PTO/SB/08) Paper No(s)		
/Thao X Le/			
Supervisory Patent Examiner, Art Unit 2892			
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Continuation of 11. does NOT place the application in condition for allowance because: The Applicant argues that the Examiner's interpretation of the areas 56a and 56b to be the test areas and contact areas, respectively, is not taught by Kim, and that Kim teaches the opposite relationship that areas 56a and 56b are contact areas and test areas, respectively. The Examiner acknowledges that Kim discloses in col. 4, lines 1-4 that the test areas are indicated as 56b. However, the recitation of a "contact" area and "test" area are statements of intended use, since they are indicating that the areas are inteded to be used as a "contact" area and a "test" area. The Examiner submits that both areas 56a and 56b may be used as either a "test" area or a "contact" area since they both connect to the underlying structure, and are able to provide both testing and contact to the underlying structure. Hence, the Exminer's maintains that the interpretation of the "test" area and "contact" to be areas 56a and 56b is valid. It has been held by the courts that a recitation with respect to the manner in which a claimed apparatus is intended to be employed does not differentiate the claimed apparatus from a prior art apparatus satisfying the claimed structural limitations. Ex parte Masham 2 USPQ2d 1647 (1987). See MPEP 2114.

The Applicant aslo argues that the combination of the sealing of the test areas of Takemae with the device of Kim would render the device of Kim inoperable. The Examiner respectfully disagrees. The Examiner submits that if "test" ares 56a were sealed, as rendered obvious by Takemae, that that device of Kim will still operate, since sealing the areas 56a of Kim will not alter the underlying structure. Whether or not the device is intended to be connected to an external circuit is a matter of indended use. One of ordinary skill in the art will recognize that the areas may be sealed to prevent further contamination, or they may be sealed and then again unsealed if one of ordinary skill in the art intends to connect them to an external device in the future. The Examiner maintains that Kim in view of Takemae renders obvous the STRUCTURE, and that the indended use of the structure is not germane to the claimed structure limitations, as per MPEP 2114.

The Applicant also argues that Kim teaches away from sealing the test areas by stating that Kim discloses cutting the wafer such that areas 56b are cut away. The Examiner acknowledges that Kim teaches the cutting away of areas 56b, however, Kim does not disclose anywhere that areas 56a (which the examiner interprets as the test ares) cannot be sealed. Furthermore, the Examiner submits that sealing the area 56a is rendered obvious by Takamae, with at least the purpose of protecting the areas from contamination. The Examiner submits that Kim does not teach away from sealing the areas 56a or 56b.

Furthermore, the Examiner notes that figure 5 of Kim is very similar to figure 2 of the current application, and that the difference seems to be only the use of the areas 1 and 2 of figure 2 of the current application as "contact" and "test" areas, respectively, which is the opposite relationship as disclosed by Kim. The Examiner submits that Kim in view of Strauss and Takemae disclose the structure, and that the use of the areas as "contact" areas and "test" areas is merely a statement of intended use, and that one of ordinary skill in the art will recognize that one may use either as a "test" area or a "contact" area.

Still further, the claims recite a "passive region" and and "active region" on the chip, that the contact areas are on the passive region, and the test areas are on the active region. The Examiner notes that the term "region" is a term of broad scope, and that any area or section of the device may be interpreted as a "region". For example, in figure 5 of Kim, the entire region 34 may be interpreted as an "active" region since there are elements of active components within this region (e.g. p-well in an n-substrate, which may be considered a pn-junction diode) and that the "passive" region may be considered simply the top of layer 53, since layer 53 is an insualting layer. Hence, one may reinterpret in Kim the "contact" areas as areas 56a on "passive" region of layer 53, and the "test" areas as areas 56b on an "active" region 34. With such an interpretation, Kim in view of Strauss and Takemae still renders obvious the claimed invention, the rejections are still maintained, and the arguments presented in amendment filed 24 September 2010 are rendered moot..